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(54) Title: METHOD AND APPARATUS FOR TRANSMITTING DATA WITH REDUCED COUPLING NOISE

(57) Abstract: A first integrated circuit is coupled to a first connector. A second connector is coupled to the first connector through multiple conductors, in which alternating pairs of conductors are reversed. A second integrated circuit is coupled to the second connector through a second group of conductors. The first integrated circuit includes multiple differential drivers and the second integrated circuit includes multiple differential receivers. The inductive coupling coefficient of the first device is modified to be substantially the same as the inductive coupling coefficient of the second device.

METHOD AND APPARATUS FOR TRANSMITTING DATA
WITH REDUCED COUPLING NOISE

5 **TECHNICAL FIELD**

The present invention relates to the transmission of data. More specifically, the present invention reduces coupling noise between conductors by using particular routing patterns for the conductors and/or modifying the coupling such that the transmitter coupling is approximately the same as the receiver coupling.

10

BACKGROUND OF THE INVENTION

Transmission lines often suffer from crosstalk which results from the parasitic coupling between nearby conductors. The coupling between the conductors can be either capacitive or inductive in nature and is typically associated with integrated circuit packages, connectors, and printed circuit traces positioned in parallel with one another.

A particular type of data transmission system transmits signals differentially along pairs of conductors. A differential driver receives data at an input and generates a corresponding differential signal on a pair of outputs (i.e., an inverting output and a non-inverting output). The differential signal is transmitted across a pair of conductors and received by a differential receiver, which rejects signals that are present on both conductors (referred to as common mode signals). This differential transmission system helps reduce noise that is induced on both conductors, however coupling noise still exists on the conductors. For example, a differential pair of conductors is likely to have noise when passing through a connector. As transmission rates increase, the resulting increase in signal edge rates

will aggravate this problem.

Existing techniques to reduce coupling between conductors include adding ground conductors between signal conductors or positioning the signal conductors farther away from one another. However, the addition of ground conductors between signal conductors increases the number of conductors, thereby increasing the cost and complexity of the system. Further, if the conductors are traces on a printed circuit board, the addition of ground conductors between signal conductors increases the printed circuit board area required to route all of the conductors. Positioning the signal conductors farther away from one another increases the size of the printed circuit board, connector, integrated circuit package, or other device that handles the conductors.

Fig. 1 illustrates a prior art system in which inductive coupling occurs between various conductors. The system uses differential signaling to transmit three bits of data across three differential line pairs. The inductors shown coupled in series with the various conductors represent the inductances inherent in the conductors. The three data bits are designated as a, -a, b, -b, c, and -c. An integrated circuit 100 receives or generates the three data bits that are provided to differential drivers 108, 110, and 112. The signals generated by the differential drivers 108-112 are transmitted through a connector 102, another connector 104, to another integrated circuit 106. Each of the three pairs of conductors is coupled to one of three differential receivers 114, 116, or 118, which are contained in integrated circuit 106. For example, the system shown in Fig. 1 may be a backplane having the connectors 102 and 104 mounted to the backplane. In this example, integrated circuits 100 and 106 are mounted on a substrate (such as a printed circuit board) which is electrically coupled to one of the connectors 102, 104.

In the example of Fig. 1, coupling noise is caused by inductive coupling. This coupling corresponds to parasitic mutual inductance which occurs due to the physical construction of the integrated circuit packages and connectors in the system. In Fig. 1, the mutual inductance is represented by inductive coupling coefficient K1, K2, and K3. Since the two connectors 102 and 104 are the same, they have the same inductive coupling coefficient (K2). The two integrated circuit packages 100 and 106 are different, so they each have a different inductive coupling coefficient (K1 and K3).

The signal quality in the system of Fig. 1 is degraded due to the parasitic mutual inductance. For example, if signals a, -a and c, -c switch while signals b, -b are quiet, a noise signal will appear at the receiver end of the line carrying b, -b. This type of noise signal reduces the timing and voltage margins of data arriving at the differential receiver 114, 116 or 118.

The system and method described herein addresses these and other problems by reducing the coupling noise on conductors positioned near one another.

SUMMARY OF THE INVENTION

In one embodiment, a first device is coupled to a first connector. A second connector is coupled to the first connector through multiple conductors, in which alternating pairs of conductors are reversed. A second device is coupled to the second connector through a second group of multiple conductors.

In another embodiment, the first device includes multiple differential drivers to transmit differential signals.

In a particular implementation of the system, the second device includes multiple differential receivers to receive multiple differential signals.

Another embodiment provides that the inductive coupling of the first device is substantially the same as the inductive coupling of the second device.

In a particular embodiment, alternating pairs of conductors in the second group of multiple conductors are reversed.

5 In a described implementation, a transmitter package is modified such that the coupling coefficient of the transmitter package is substantially the same as the coupling coefficient of a receiver package. Multiple pairs of differential signals are transmitted using the transmitter package. The receiver then receives and decodes the multiple pairs of differential signals.

10

BRIEF DESCRIPTION OF THE DRAWINGS

Fig. 1 illustrates a prior art system in which inductive coupling occurs between various conductors.

Fig. 2 illustrates an embodiment of a system that reduces coupling noise.

15 Fig. 3 illustrates a pair of connectors coupled together with multiple pairs of transmission lines.

Fig. 4 is a flow diagram illustrating a procedure for reducing coupling noise between conductors.

20 Fig. 5 illustrates another embodiment of a system that reduces coupling noise between conductors.

Fig. 6 is a flow diagram illustrating another procedure for reducing coupling noise between conductors.

DETAILED DESCRIPTION OF THE PREFERRED EMBODIMENT

25 The method and apparatus for transmitting data with reduced coupling noise (both inductive coupling and capacitive coupling) described herein routes the signal

conductors in a particular manner to reduce the inductive coupling between signal conductors. Additionally, the transmitter coupling is modified to be approximately the same as the receiver coupling to reduce the inductive coupling between signal conductors. Particular examples herein are described with reference to integrated circuits and connectors. However, the teachings of the present invention can be applied to any type of device coupled to any type of interconnection mechanism.

Fig. 2 illustrates an embodiment of a system that reduces inductive coupling noise. An integrated circuit 200 receives or generates three data bits (labeled a, -a, b, -b, c, and -c), and contains three differential drivers 208, 210, and 212. Each of the three pairs of signal conductors is coupled to one of the three differential drivers. Differential drivers 208-212 may also be referred to as differential transmitters. The integrated circuit 200 can be any type of integrated circuit such as a memory device, a processor, an ASIC, or programmable logic devices. The signal conductors used to transmit data may include wires, cables, traces on a printed circuit board, conductors embedded within a substrate (e.g., between different layers of a substrate), and various other conductive materials.

The signals generated by the differential drivers 208-212 are transmitted through a connector 202, another connector 204, to another integrated circuit 206. Integrated circuit 206 contains three differential receivers 214, 216, and 218, each coupled to receive a different pair of signal conductors. Differential receivers 214-218 may also be referred to as differential decoders. The connectors 202 and 204 can be any type of electrical connecting device capable of coupling with at least one component, such as a mating connector, a substrate, an edge of a printed circuit board, etc.

In a particular embodiment, the system of Fig. 2 is a backplane having multiple connectors (e.g., connectors 202 and 204) electrically coupled to the

backplane. The backplane is a printed circuit board, which can be manufactured using any type of substrate capable of supporting the multiple connectors and multiple conductors that interconnect the multiple connectors. The backplane may be a multi-level board having multiple layers of substrate materials and/or
5 conductive materials. In this embodiment, the integrated circuits 200 and 206 are mounted on another substrate (e.g., a printed circuit board or "card") that is electrically coupled to the corresponding connector 202 or 204. In a particular configuration, integrated circuits 200 and 206 are memory devices (either volatile or non-volatile memory devices), each of which is mounted on a printed circuit
10 board. The memory device and printed circuit board combination may be referred to as a memory module. Any number of memory modules can be electrically coupled to connectors mounted to a backplane.

As shown in Fig. 2, the two outer differential pairs (i.e., a, -a and c, -c) have a polarity reversal (also referred to as a "twist" or a conductor reversal) between the
15 two connectors 202 and 204. This polarity reversal significantly reduces (or eliminates) the coupling noise of the two connectors 202 and 204 because any coupling noise added by connector 202 is subtracted by connector 204. The polarity is reversed again between connector 204 and integrated circuit 206. This second polarity reversal ensures that the differential receiver receives the inverted
20 and non-inverted signals on the appropriate input pins.

In an alternate embodiment, the second polarity reversal is not necessary because the polarity of the input connections to the differential receiver is reversed. For example, in the orientation shown in Fig. 2, without the second polarity reversal, the "a" signal (non-inverted) would be coupled to the inverted input to the
25 differential receiver 214 and the "-a" signal (inverted) would be coupled to the non-inverted input to the differential receiver 214. However, if the orientation of the

differential receiver 214 is reversed such that the positions of the inputs to the differential receiver are reversed, then the second polarity reversal of the signal conductors is not necessary.

Fig. 3 illustrates a pair of connectors 230 and 232 coupled together with multiple pairs of conductors. As shown in Fig. 3, alternating pairs of conductors have reversed polarity. As discussed above, this reversed polarity significantly reduces (or eliminates) the coupling noise induced by the two connectors because any coupling noise added by one connector is subtracted by the other connector. The top pair of conductors 234 and 236 have their polarity reversed. The next pair of conductors 238 and 240 are not reversed. The next pair of conductors have a reversed polarity, and the next pair is not reversed. This pattern of reversing the polarity of alternating pairs of conductors continues until the last pair of conductors. In the example of Fig. 3, seven pairs of conductors are illustrated. However, the teachings of the present invention can be applied to systems having any number of pairs of conductors coupled between two connectors.

Additionally, in the example of Fig. 3, the polarity reversals are shown approximately midway between the two connectors 230 and 232. However, in alternate embodiments, the polarity reversals may be located anywhere between the two connectors 230 and 232. Further, the polarity reversals need not occur at the same location. For example, a first polarity reversal may occur close to connector 230, a second polarity reversal may occur midway between the two connectors 230 and 232, and a third polarity reversal may occur close to the connector 232.

Fig. 4 is a flow diagram illustrating a procedure for reducing coupling noise between conductors. First, an integrated circuit generates multiple pairs of differential signals (block 250). A first connector generates inductive coupling noise as the differential signals are transmitted through the first connector (block

252). Next, the procedure reverses the polarity of alternating pairs of differential signal conductors (block 254). A second connector generates inductive coupling noise opposite the noise generated by the first connector as the differential signals are transmitted through the second connector (block 256). The procedure then
5 reverses the polarity of alternating pairs of differential signal conductors (block 258). The pairs of signal conductors reversed in block 258 are the same as those reversed in block 254. Finally, the procedure decodes the multiple pairs of differential signals.

Fig. 5 illustrates another embodiment of a system that reduces coupling noise between conductors. The system illustrated in Fig. 5 is similar to the system
10 illustrated in Fig. 2. An integrated circuit 300 includes three differential drivers 308, 310, and 312. Integrated circuit 300 may also be referred to as the transmitter package because it transmits the signals. The signals generated by the differential drivers 308-312 are transmitted through a connector 302, another connector 304, to
15 another integrated circuit 306, which includes three differential receivers 314, 316, and 318. Integrated circuit 306 may also be referred to as the receiver package because it receives the signals from the transmitter package (i.e., integrated circuit 300).

In the embodiment of Fig. 5, the polarity of alternating pairs of conductors
20 (i.e., a, -a and c, -c) is reversed between the two connectors 302 and 304. However, there is no corresponding polarity reversal between connector 304 and integrated circuit 306, as shown in Fig. 2. Also, the inductive coupling between conductors in integrated circuit 300 is K_3 , which is the same inductive coupling as in integrated circuit 306. In this embodiment, the inductive coupling noise caused
25 by the two connectors 302 and 304 is significantly reduced due to the polarity reversal in the conductors, as discussed above. Further, the inductive coupling

noise caused by the two integrated circuits 300 and 306 is reduced because the two integrated circuits have substantially the same inductive coupling coefficient.

The system shown in Fig. 5 is designed to provide symmetric coupling between the two halves of the transmission lines (i.e., integrated circuit 300 and connector 302 represent one half, and connector 304 and integrated circuit 306 represent the other half. The symmetry between the two connectors 302 and 304 is provided by the polarity reversal between the two connectors. Similarly, the symmetry between the two integrated circuits 300 and 306 is provided by the same coupling in the two integrated circuits.

10 In a particular system, the receiver package (i.e., integrated circuit 306) has more coupling than the transmitter package (i.e., integrated circuit 300). To provide the necessary symmetry, the coupling of the transmitter package is increased (i.e., more coupling noise is generated) to match the coupling of the receiver package. The coupling coefficient of the transmitter package can be increased by redesigning
15 the package of the integrated circuit so that the coupling coefficient of the transmitter package is substantially similar to the coupling coefficient of the receiver package. For example, in a typical integrated circuit package, crosstalk results from the inductive coupling between bond wires. The inductive coupling is proportional to the length of the bond wires, in which longer bond wires have
20 greater inductive coupling. Thus, the coupling coefficient of the transmitter package can be increased by increasing the bond wire length.

As shown in Fig. 5, the polarity of the conductors is not reversed between connector 304 and integrated circuit 306 (as was the case in Fig. 2). However, the orientation of the differential receivers 314 and 318 is reversed such that the
25 inverted signal (-a and -c) is coupled to the inverted input of the differential receiver and the non-inverted signal (a and c) is coupled to the non-inverted input

of the differential receiver. Note that the orientation of differential receiver 316 is not reversed because the associated pair of conductors were not reversed between the two connectors 302 and 304.

In an alternative embodiment, the orientation of differential receivers 314
5 and 318 is not changed. Thus, the inverted signals are coupled to the non-inverted inputs and the non-inverted signals are coupled to the inverted inputs. Thus, the differential receivers 314 and 318 generate incorrect outputs (i.e., the opposite of the actual value). This is easily corrected by coupling an inverter to the output of each differential receiver 314 and 318. The output of the inverter is the correct
10 output for the system.

In another embodiment, two integrated circuits (e.g., integrated circuits 300 and 306) are coupled to one another through multiple pairs of conductors (i.e., a chip-to-chip link). As mentioned above, the inductive coupling coefficient of the first integrated circuit is designed to be substantially the same as the inductive
15 coupling coefficient of the second integrated circuit. In this embodiment, no connectors are located between the two integrated circuits. This configuration provides for the reversing of polarities on alternating line pairs, in the manner discussed. Thus, instead of reversing polarities of signal lines between two connectors, this configuration reverses the polarity of signal lines coupled between
20 the two integrated circuits.

Fig. 6 is a flow diagram illustrating another procedure for reducing coupling noise between conductors. The flow diagram shown in Fig. 6 corresponds to the system shown in Fig. 5. First, the transmitter package is modified such that the coupling coefficient of the transmitter package is substantially the same as the
25 receiver package (block 350). The transmitter package generates multiple pairs of differential signals (block 352). A first connector generates inductive coupling

noise as the differential signals are transmitted through the first connector (block 354). Next, the procedure reverses the polarity of alternating pairs of differential signal conductors (block 356). A second connector generates inductive coupling noise opposite to the noise generated by the first connector as the differential signals are transmitted through the second connector (block 358). Finally, the procedure decodes the multiple pairs of differential signals (block 360).

Thus, a system has been described that reduces inductive coupling noise between conductors. The noise reduction is accomplished by reversing alternating pairs of conductors and/or modifying the coupling of the integrated circuit packages such that the transmitter coupling is substantially the same as the receiver coupling.

Although the description above uses language that is specific to structural features and/or methodological acts, it is to be understood that the invention defined in the appended claims is not limited to the specific features or acts described. Rather, the specific features and acts are disclosed as exemplary forms of implementing the invention.

CLAIMS

1. An apparatus comprising:
 - a first device;
 - a first connector coupled to the first device;
 - 5 a second connector coupled to the first connector through a first plurality of conductors, wherein alternating pairs of conductors are reversed; and
 - a second device coupled to the second connector through a second plurality of conductors.
- 10 2. An apparatus as recited in claim 1 wherein the first device includes a plurality of differential drivers.
3. An apparatus as recited in claim 1 wherein the second device includes a plurality of differential receivers.
- 15 4. An apparatus as recited in claim 1 wherein the first device is an integrated circuit.
5. An apparatus as recited in claim 1 wherein the first device is an
20 integrated circuit disposed on a substrate, wherein the substrate is electrically coupled to the integrated circuit and the first connector.
6. An apparatus as recited in claim 1 wherein the second device is an integrated circuit.

7. An apparatus as recited in claim 1 wherein the first device has an inductive coupling coefficient substantially the same as the inductive coupling coefficient of the second device.

5 8. An apparatus as recited in claim 1 wherein the alternating pairs of conductors are reversed once between the first connector and the second connector.

9. An apparatus as recited in claim 1 wherein alternating pairs of conductors in the second plurality of conductors are reversed.

10

10. An apparatus comprising:

a first integrated circuit including a plurality of differential drivers;

a first connector coupled to the first integrated circuit;

a second connector coupled to the first connector through a plurality of
15 electrical conductors, wherein alternating pairs of the electrical conductors are reversed; and

a second integrated circuit coupled to the second connector, wherein the second integrated circuit includes a plurality of differential receivers.

20 11. An apparatus as recited in claim 10 further comprising a second plurality of electrical conductors coupled between the second connector and the second integrated circuit, wherein alternating pairs of the second plurality of electrical conductors are reversed.

12. An apparatus as recited in claim 10 further comprising a second plurality of electrical conductors coupled between the second connector and the second integrated circuit, wherein each pair of conductors includes an inverted conductor and a non-inverted conductor, each inverted conductor coupled to a non-inverted input of one of the differential receivers, and each non-inverted conductor coupled to an inverted input of one of the differential receivers.

13. An apparatus as recited in claim 10 wherein the first integrated circuit has an inductive coupling coefficient substantially the same as the inductive coupling coefficient of the second integrated circuit.

14. An apparatus as recited in claim 10 wherein the alternating pairs of electrical conductors are reversed once between the first connector and the second connector.

15

15. An apparatus comprising:
a printed circuit board;
a plurality of connectors disposed on the printed circuit board;
a first integrated circuit disposed on a first substrate, wherein the first substrate is configured to be coupled to one of the plurality of connectors;
a second integrated circuit disposed on a second substrate, wherein the second substrate is configured to be coupled to one of the plurality of connectors;
and
a first plurality of electrical conductors coupled to the plurality of connectors, wherein alternating pairs of conductors between adjacent connectors have reversed polarity.

16. An apparatus as recited in claim 15 wherein the printed circuit board is a backplane.

5 17. An apparatus as recited in claim 15 further comprising a second plurality of conductors coupled between the first integrated circuit and one of the plurality of connectors, wherein alternating pairs of conductors have reversed polarity.

10 18. An apparatus as recited in claim 15 wherein the first substrate is a printed circuit board.

19. An apparatus as recited in claim 15 wherein the first substrate is a memory module.

15

20. An apparatus as recited in claim 15 wherein the first integrated circuit is a memory device.

21. An apparatus as recited in claim 15 wherein the first integrated circuit
20 has an inductive coupling substantially the same as the inductive coupling of the second integrated circuit.

22. An apparatus comprising:
a first device; and
25 a second device coupled to the first device through a plurality of electrical conductors, wherein the inductive coupling coefficient of the first device is adjusted

to be substantially the same as the inductive coupling coefficient of the second device.

23. An apparatus as recited in claim 22 wherein the first device includes
5 a plurality of differential drivers, wherein each differential driver is coupled to a pair of electrical conductors.

24. An apparatus as recited in claim 22 wherein the second device
includes a plurality of differential receivers, wherein each differential receiver is
10 coupled to a pair of electrical conductors.

25. An apparatus as recited in claim 22 further comprising a pair of
connectors coupled between the first device and the second device, wherein a
second plurality of electrical conductors are coupled between the pair of connectors,
15 and wherein alternating pairs of electrical conductors are reversed.

26. A method comprising:
generating a plurality of differential signals;
transmitting the plurality of differential signals through a first connector and
20 a second connector to a plurality of differential receivers;
reversing the polarity of alternating differential signals between the first and
second connectors; and
reversing the polarity of alternating differential signals between the second
connector and the plurality of differential receivers.

27. A method as recited in claim 26 wherein the first connector generated inductive coupling noise as the differential signals are transmitted through the first connector.

5 28. A method as recited in claim 26 wherein the second connector generated inductive coupling noise opposite the noise generated by the first connector as the differential signals are transmitted through the second connector.

10 29. A method as recited in claim 26 further including decoding the plurality of differential signals.

30. A method as recited in claim 26 wherein a transmitter package transmits the plurality of differential signals and a receiver package receives the plurality of differential signals.

15

31. A method as recited in claim 30 further including modifying the transmitter package such that the coupling coefficient of the transmitter package is substantially the same as the receiver package.

32. A method comprising:

modifying a transmitter package such that the coupling coefficient of the transmitter package is substantially the same as the coupling coefficient of a receiver package;

5 transmitting multiple pairs of differential signals using the transmitter package; and

receiving the multiple pairs of differential signals using the receiver package.

33. A method as recited in claim 32 wherein the transmitter package
10 transmits multiple pairs of differential signals across a plurality of conductors.

34. A method as recited in claim 32 further comprising decoding the multiple pairs of differential signals.

15 35. A method as recited in claim 32 wherein the differential signals are transmitted through a pair of connectors on a plurality of conductors, wherein alternating pairs of conductors are reversed between the pair of connectors.

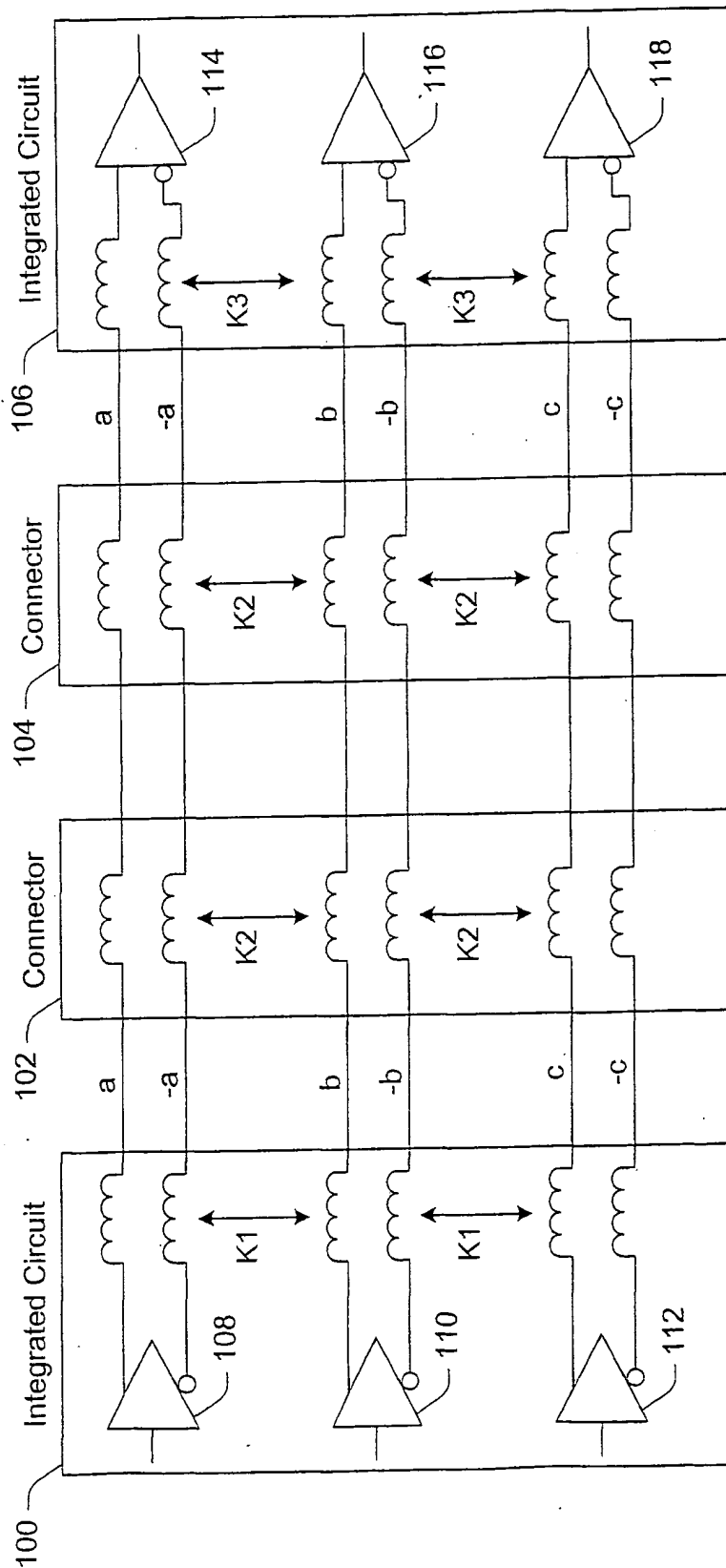


Fig. 1
(Prior Art)

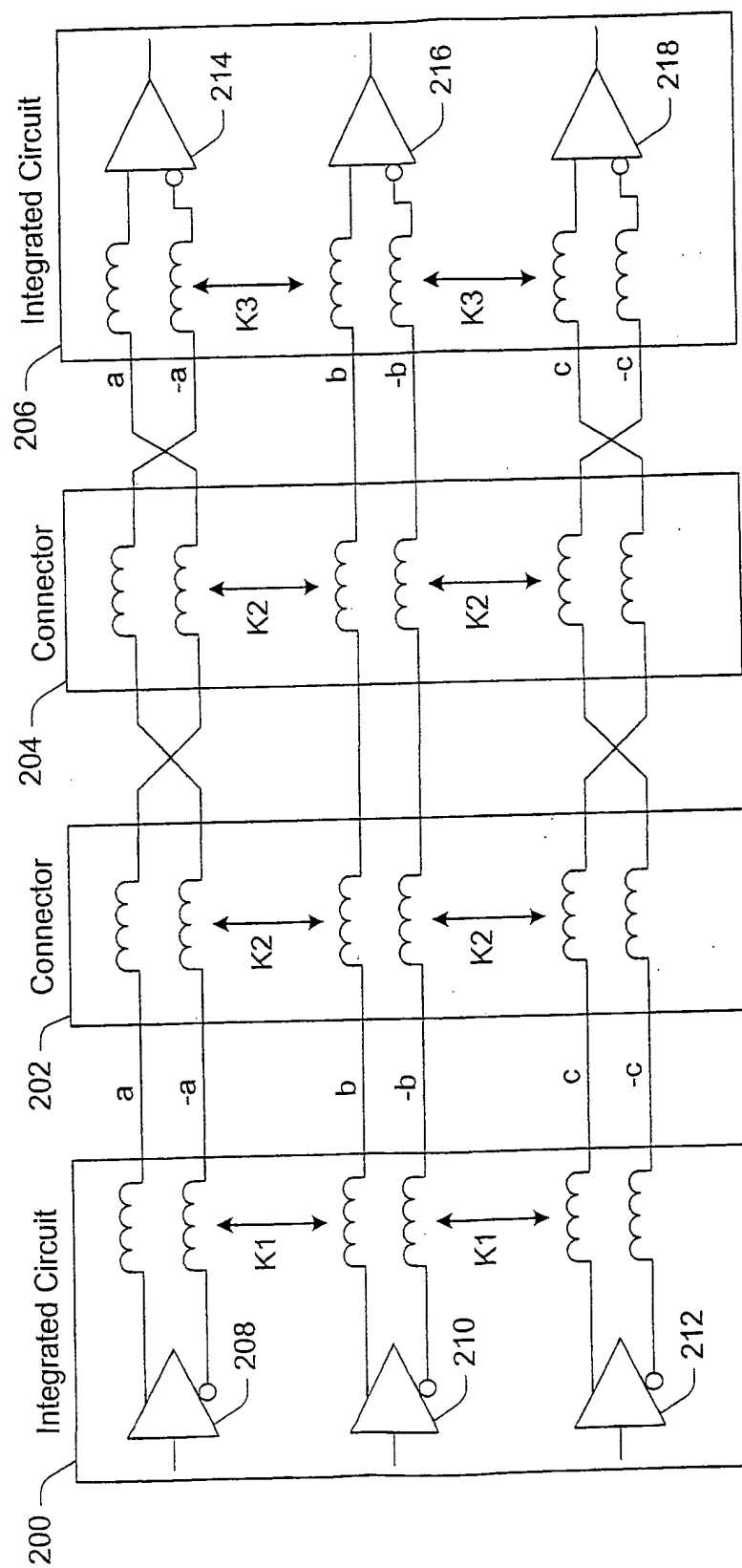


Fig. 2

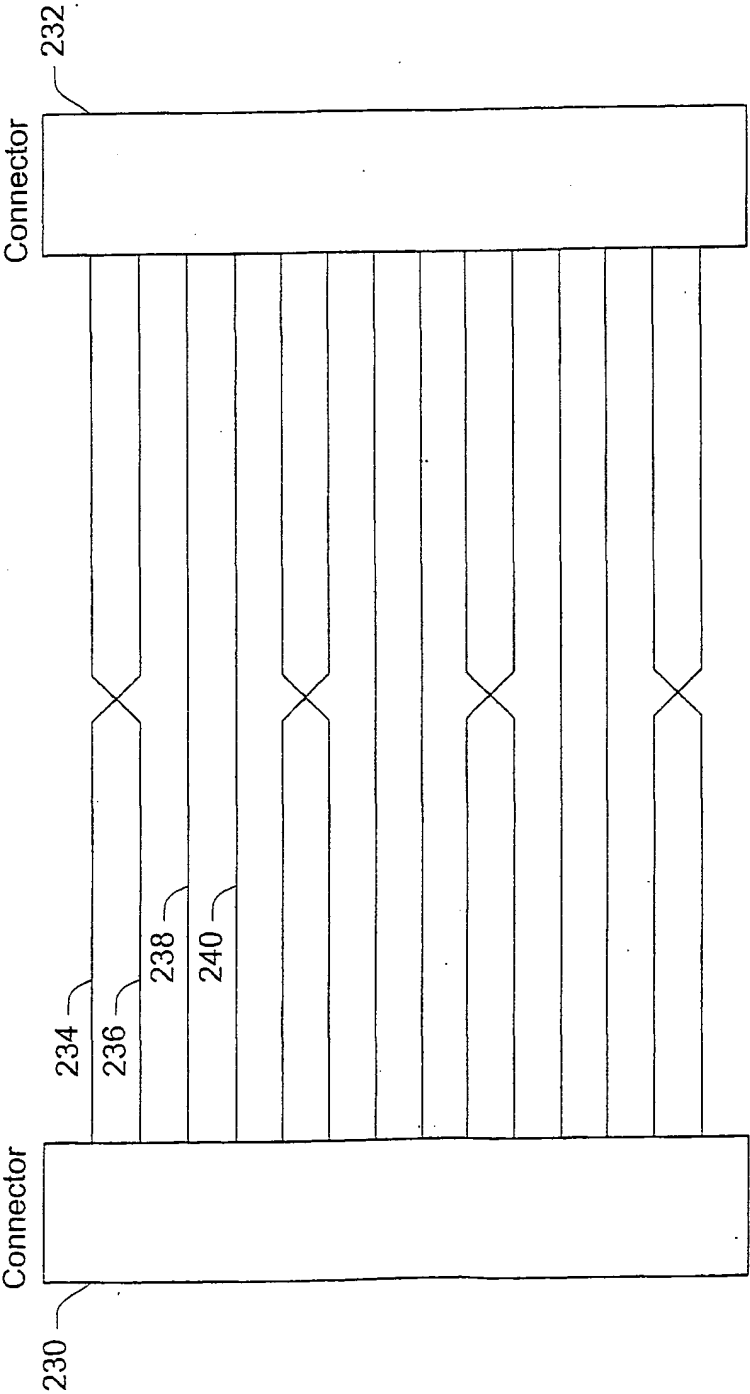
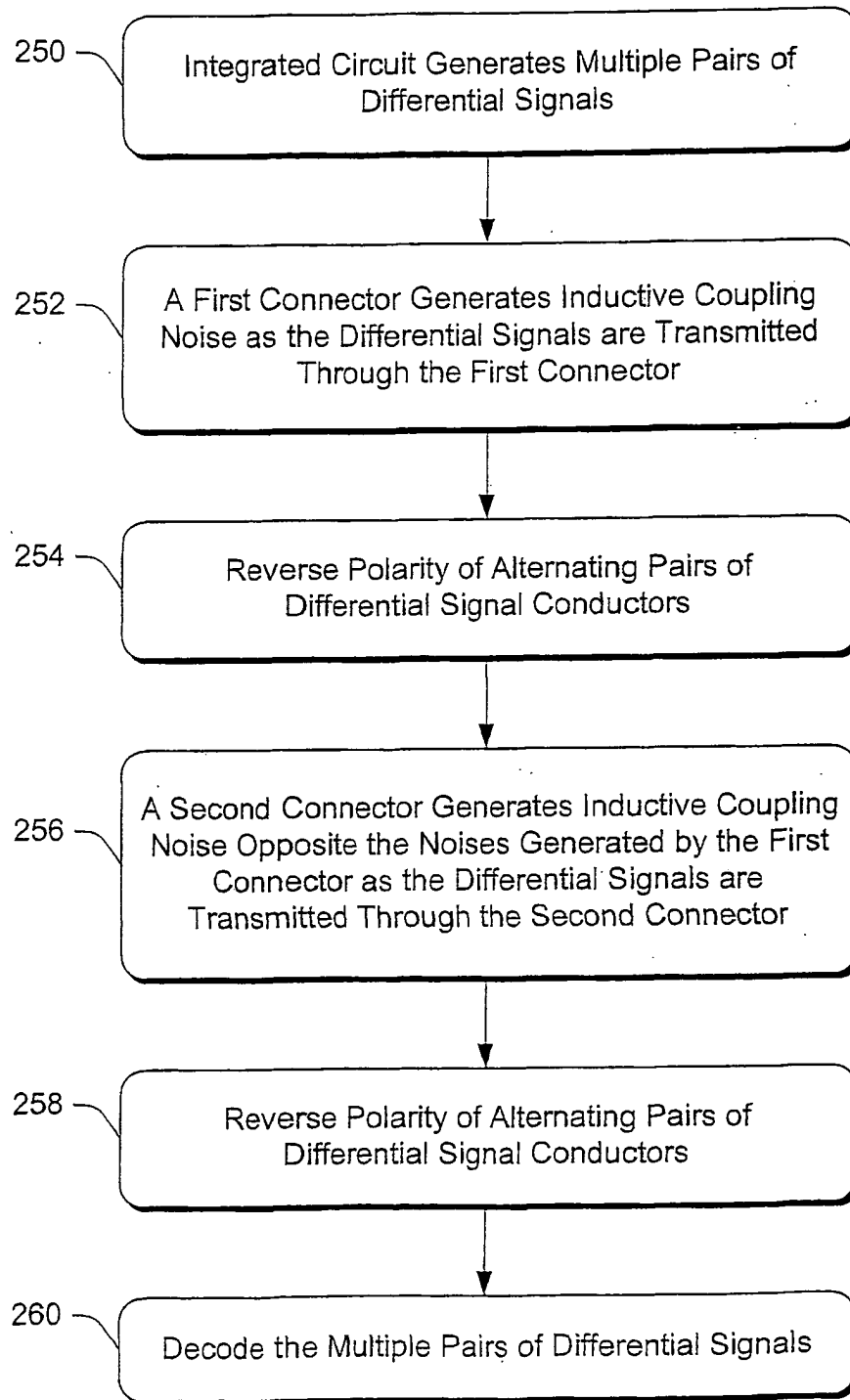


Fig. 3

*Fig. 4*

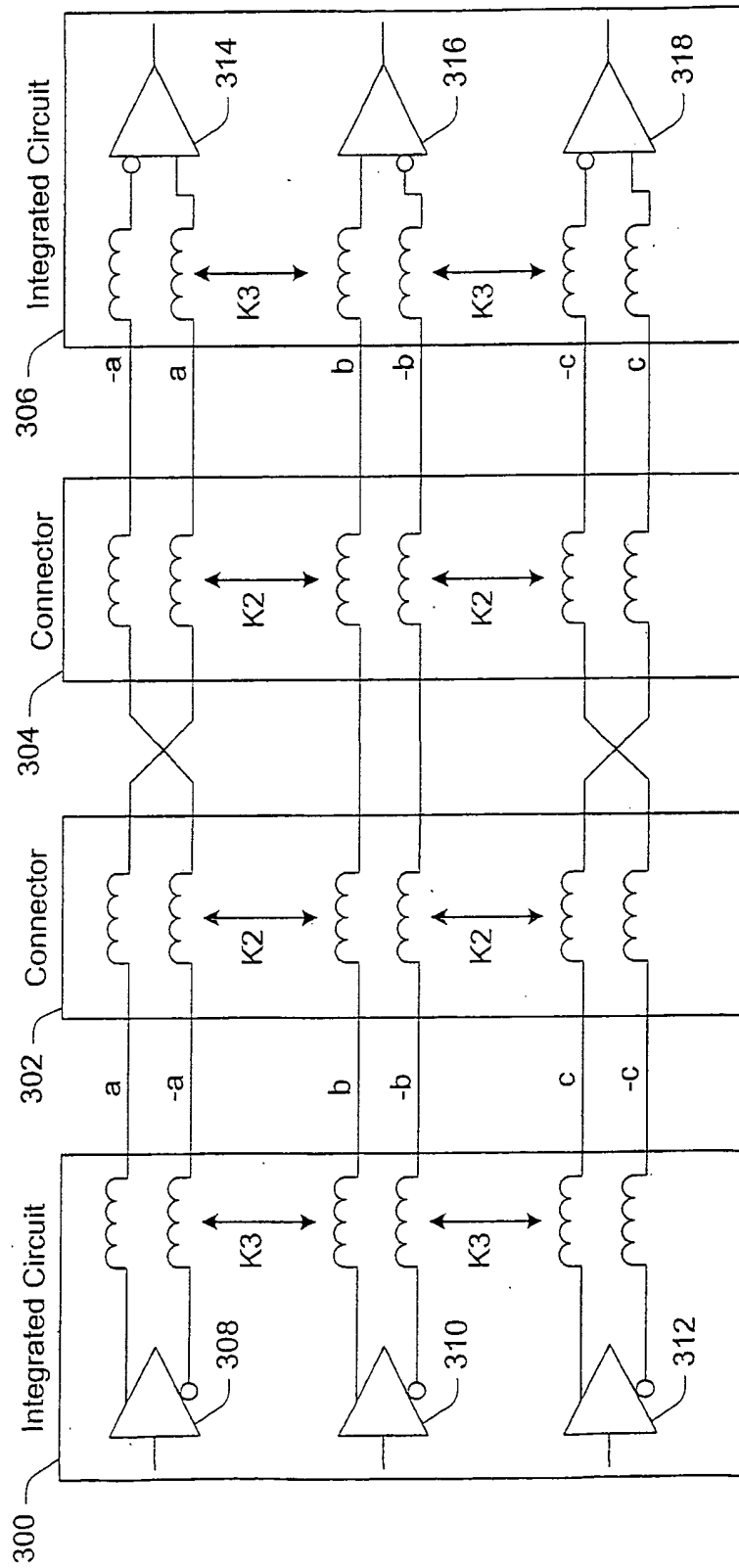
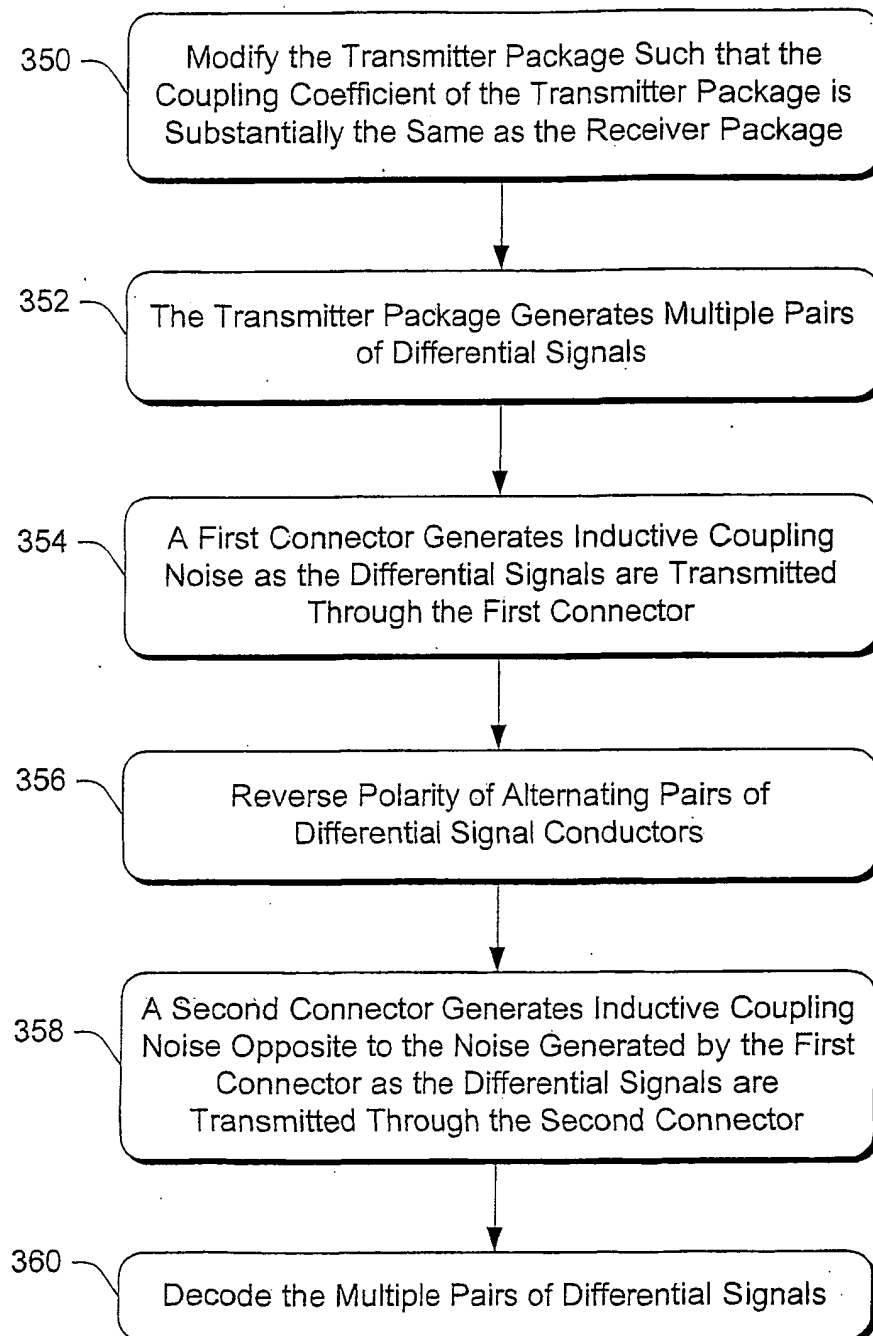


Fig. 5

*Fig. 6*